

Docket No.: GR 99 P 2378



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By: Mark P. ~

Date: 12/4/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Xiaopin Zhang et al.
Application No. : 09/624,438
Filed : July 24, 2000
Title : Controllable Current Source Circuit and Phase Locked Loop
Equipped Therewith
Art Unit : 2787

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Floyd M. Gardner: "Charge-Pump Phase-Lock Loops", IEEE Transactions on Communications, Vol. COM-28, No. 11, November 1980, pp. 1849-1858;

Taizo Yamawaki et al.: "A 2.7-V GSM RF Transceiver IC", IEEE Journal of Solid State Circuits, Vol. 32, No. 12, December 1997, pp. 2089-2096.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

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For Applicants

Date: December 4, 2000

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